

## PAPERS ON HARDWARE ACCELERATION

1. S. Patel and W.-M. W. Hwu, "[Accelerator Architectures](#)", *IEEE Micro*, vol. 28, n. 4, p. 4-12, 2008.
2. M. B. Taylor, "[A Landscape of the New Dark Silicon Design Regime](#)", *IEEE Micro*, vol. 33, n. 5, p. 8-19, 2013.
3. D. Buell, T. El-Ghazawi, K. Gaj and V. Kindratenko, "[High-Performance Reconfigurable Computing](#)", *IEEE Computer*, p. 23-27. Mar. 2007.

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4. M. C. Herbordt et al., "[Achieving High Performance with FPGA-Based Computing](#)", *IEEE Computer*, p. 50-57. Mar. 2007.
5. H. Esmailzadeh et al., "[What is Happening to Power, Performance, and Software?](#)", *IEEE Micro*, vol. 32, n. 3, p. 110-121, 2012.
6. L. A. Barroso and U. Holzle, "[The Case for Energy-Proportional Computing](#)", *IEEE Computer*, p. 33-37. Dec. 2007.
7. E. Lindholm, J. Nickolls, S. Oberman and J. Montrym, "[Nvidia Tesla: a Unified Graphics and Computing Architecture](#)", *IEEE Micro*, vol. 28, n. 2, p. 39-55, 2008.

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8. A. Putnam et al., "[A reconfigurable fabric for accelerating large-scale datacenter services](#)", *Proc. of ACM/IEEE 41st International Symposium on Computer Architecture (ISCA)*, p. 13-24. Jun. 2014.
9. E. Nurvitadhi et al., "[Can FPGAs Beat GPUs in Accelerating Next-Generation Deep Neural Networks?](#)", *Proc. of FPGA'17*, p. 5-14. Feb. 2017, Monterey, USA.

## Technology Articles & White Papers

10. S. K. Moore, "[3 Paths to 3D Processors](#)", *IEEE Spectrum*, June 2022.  
**together with**  
M. S. Smith, "[Single-Chip Processors Have Reached Their Limits](#)", *IEEE Spectrum*, July 2022.
11. Cerebras Systems, "[Cerebras Systems White Paper](#)", *White Paper 02*, 2021.
12. nVIDIA, "[NVIDIA H100 Tensor Core GPU Architecture](#)", *White Paper*, 2022.
13. Tesla, "[Tesla Dojo Technology](#)", *White Paper*, 2021.